



## Call for Papers

Electronic Design Automation (EDA) ecosystem plays a crucial role in the development of intelligent solutions: from connected smart IoT devices to Cloud-based Big Data Analytics Systems. The EDA track of COINS provides a central location for all members of the ecosystem to connect, share, learn and witness cutting edge research in all areas of development for EDA in IoT, Artificial Intelligence, and Big Data Analytics sectors. This track encourages original and high-quality submissions related to one or more of the following topics (but not limited to):

- 1) System Design, High-Level Synthesis and Optimization
- 2) Machine Learning in EDA Methodologies
- 3) High-Level, Behavioral, and Logic Synthesis and Optimization
- 4) Device Modeling, System Simulation and Validation
- 5) Design for Reliability and Manufacturability
- 6) Formal Methods and System Verification
- 7) Cell-Library Design, Partitioning, Floor-planning, Placement
- 8) Clock Network Synthesis, Power Delivery Network Design, Routing, and Post-Layout Optimization and Verification
- 9) Design and Test for Analog and Mixed-Signal Systems and Circuits
- 10) Power Modeling, Optimization and Low-Power Design
- 11) Temperature and Variability Aware Design and Optimization
- 12) EDA for Biological/Brain Inspired Systems and Electronics
- 13) CAD for Cyber-Physical Systems

## Organizing Committee

### Track Chairs

**Aida Todri-Sanial**, CNRS-LIRMM

*/University of Montpellier, France*

**Yuanqing Cheng**, Beihang University,  
China

### Key Dates



Submission of Regular Papers and Special Session Papers  
**March 06, 2020**



Notification of Paper Acceptance  
**May 06, 2020**



Camera-Ready Submission  
**May 16, 2020**

