

Hybrid Event:
(On-site In-person Presentation & Virtual Presentation)

Barcelona, Spain
August 23-25, 2021



**Call for
Papers**

Special Session on Critical System Design

In the context of the COINS conference, this special session focuses on all challenges in the development and operation of critical IoT systems. A critical system in the context of this special session is a networked HW/SW system that must be highly reliable and retain its reliability if the IoT system or its environment evolves over time. This session covers all type of critical systems, including safety, security, mission and business critical systems and ultimately covers systems where a mixture of different criticalities has to be considered on a network of potentially small and resource constrained IoT nodes.

Since IoT devices are expected to operate over a long time (in potentially harsh environment conditions), contributions considering different phases in the DevOps ("development" and "operations") cycle from specification, design and verification, over deployment, configuration and monitoring to updates in-the-field are of high interest. If possible (this is not mandatory), contributions to this special session should address two potential conflicting properties, such as: Safety and security

- Real-time and low power
- Real-time, low power and security

...

This session is open for classic research papers, experimental research and industrial or academic case-studies. If in doubt, whether your paper fits into this special session, please contact the special session chair kim.gruettner@offis.de.

Special Session Chairs

Kim Grüttner, OFFIS, DE

Track Chairs

José Mikel Azkarat e-askatsua, IK4-IKERLAN, ES

Patricia Balbastre, U PV & FentISS, ES

Oliver Bringmann, U Tübingen, DE

Gerhard Fohler, TU Kaiserslautern, DE

Kees Goossens, TU/e, NL

Yanqiu Huang, U Bremen, DE

Philipp Ittershagen, OFFIS, DE

Leonidas Kosmidis, BSC, ES

Renato Mancuso, Boston University, USA

Daniel Müller-Gritschneider, TU Munich, DE

Roman Obermaier, U ni Siegen, DE

Michael Paulitsch, Intel, DE

Davide Quaglia, U Verona, IT

Ingo Sander, KTH, SE

Herbert Taucher, Siemens AG, DE

Sascha Uhrig, Airbus, DE

Eugenio Villar, U Cantabria, ES

Davide Zoni, Politecnico di Milano, IT