

IEEE COINS 2023

IEEE International Conference on Omni-layer Intelligent Systems

IEEE | IEEE RAS | IEEE CEDA | IEEE COMPUTER SOCIETY | VSA-TC & NOAS TC IEEE CAS | E-HEALTH-TC IEEE COMSOC | CONTROL, ROBOTICS, AND MECHATRONICS TC & CLOUD AND WIRELESS SYSTEMS FOR INDUSTRIAL APPLICATIONS TC IEEE IES | IEEE IOT

Berlin, Germany | Hybrid

July 23-25, 2023

<https://coinsconf.com>



Call for
Papers

Track: Circuits and Systems (CAS) Designs for AI & IoT

The Internet of Things (IoT) and Big Data applications drive the machine learning technology development. The devices for the IoT applications provide sensing, actuation, processing, and communication at low-power levels and at low cost. Thereby, they must be resilient in the face of harsh environments, challenging communication requirements, and long lifetimes that may reach beyond the useful lives of the individual nodes. This track explores the design of circuits and systems for the future IoT era.

Emerging IoT devices and applications produce increasingly high volumes of data. At the same time, they require significant computational requirements that often do not fit in the stringent power envelopes of the existing IoT devices. Through the hierarchical IoT structure, the datacenters and cloud servers are usually used to empower the IoT systems by performing massive data processing on behalf of the IoT users. In this track, the emerging hierarchical IoT structure and its cross-layer collaboration schemes to process the massive data will be investigated.

Artificial Neural Networks (ANN) and Spiking Neural Networks (SNN) have shown significant advantages in many domains. Current large-scale ANNs, however, involve complex communication, extensive computations, and large storage requirements, which are beyond the capability of current resource-constrained IoT devices. SNNs, on the other hand, process data asynchronously and can lead to efficient signal processing systems. However, learning in these systems has not shown the same level of success as ANNs and need further investigations. We invite original research ideas and efforts toward efficient design of ANN and SNN circuits and systems for low-power and high-performance IoT devices.

The track topics of interest include, but are not limited to:

- Sensory Circuits & Systems for IoT
- Communications Circuits & Systems for IoT
- Energy-aware Circuits and Systems for IoT Applications
- Circuits & Systems for Big Data Processing
- Artificial Intelligence Circuits and Systems
- Neural Networks & Neuromorphic Engineering
- Emerging Technologies in CAS (e.g., Beyond CMOS)
- Application and Architecture of Artificial Neural Networks

Track Chair

Kun-Chih Chen (Jimmy), National Yang Ming Chiao Tung University, Taiwan

TPC Members

Abhijit Das, Inria, France

Anagnostopoulos Iraklis, Southern Illinois University, USA

Bevan M. Baas, University of California, Davis, USA

Chun-Hsian Huang, National Taitung University, Taiwan

José Cano Reyes, University of Glasgow, Scotland

Mayank Parasar, Samsung, USA

Md Farhadur Reza, Eastern Illinois University, USA

Midia Reshadi, Trinity College Dublin, Ireland

Seyed Morteza Nabavinejad, Worcester Polytechnic Institute, USA

Yean-Ru Chen, National Cheng Kung University, Taiwan